

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: LAMARRE, Philip A.

Group Art Unit: 1756

Serial No. 10/710,023

Examiner: DUDA, Kathleen

Filed: 06/14/2004

Dkt No: 20030146-US

For: LITHOGRAPHIC SEMICONDUCTOR MANUFACTURING USING A
MULTI-LAYERED PROCESS

To: Mail Stop ISSUE FEE
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

From:

42716

Commissioner:

LETTER OF TRANSMITTAL

1. Transmitted herewith is 1 page PTOL-85B Issue Fee Transmittal, 1 page PTO/SB/47 Fee Address Form, and 12 sheets of Replacement/Formal Drawings.

2. DEPOSIT ACCOUNT **190130** AUTHORIZATION – The Office is hereby authorized to charge the required fee, and any deficiency or credit any overpayment in the fees to the above deposit account, owned by BAE SYSTEMS Information and Electronic Systems Integration Inc. an authorized signator for which is Kevin M. Perkins, V.P. & Company Counsel, and for whom the undersigned are authorized agents.

Respectfully submitted,

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